

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which generates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and further quadrature-demodulating a result of the A/D-converting, and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which updates a compensation vector determined in advance, on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value of ~~the~~ a product of ~~the~~ an inner product between an increment vector indicating ~~the~~ an increment of said deviation with different sample times in chronological order ~~in the order of time series~~ and the compensation vector, and the latest deviation vector indicating the deviation, and which adds the updated compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum.

2. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which creates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and by quadrature-demodulating the result of the A/D-converting and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which determines a compensation vector on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value of ~~the~~ a product of ~~the~~ an inner product between said input signal and said vector signal and an increment vector indicating ~~the~~ an increment of said deviation with different sample times in chronological order ~~in the order of time series~~, and which adds the compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum.

3. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which creates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and by quadrature-demodulating the result of the A/D-converting and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which updates ~~the~~ a compensation vector determined in advance, on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value which is of the a product of ~~the sum of the~~ an integrated value and a chronologically latest deviation vector, ~~inner product in a vector space between an increment vector indicating the increment of said deviation in the order of time series and the compensation vector, and the latest deviation vector indicating the deviation~~, and which adds the updated compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum, wherein the integrated value is a sum of past inner products being integrated, in which each of the past inner products is an inner product of an increment vector indicating an increment of said deviation with different sample times in chronological order and the compensation vector determined in advance.

4. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which creates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and by quadrature-demodulating the result of the A/D-converting and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which ~~determines~~ updates a compensation vector on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value ~~of the~~ which is a product of ~~the sum~~ an integrated value and an increment vector, in a vector space between the inner product of said input signal and said vector signal and an increment vector indicating the increment of said deviation in the order of time series, and which adds the updated compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum, wherein the integrated value is a sum of past inner products being integrated, in which each of the past inner products is an inner product of said input signal and said vector signal, and the increment vector indicates an increment of said deviation with different sample times in chronological order.

5. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which creates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and by quadrature-demodulating the result of the A/D-converting and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which subtracts from said vector signal the inner product between an increment vector indicating ~~the~~ an increment of said deviation with different sample times in chronological order ~~in the order of time series~~ and ~~the~~ a compensation vector determined in advance, which updates the compensation vector on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value of the latest deviation vector indicating the deviation, and which adds the updated compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum.

6. (Currently Amended) An offset compensating device comprising:

a deviation monitor unit which creates a vector signal by A/D-converting ~~the~~ a vector sum of the results of processings applied to two quadrature AC signals individually in response to an input signal and by quadrature-demodulating the result of the A/D-converting and which monitors ~~the~~ a deviation of the DC components superposed on the vector signal; and

an adaptive control unit which subtracts ~~the~~ an inner product between said input signal and said vector signal from said vector signal, which updates ~~the~~ a compensation vector on the basis of an adaptive algorithm to minimize ~~the~~ an expectation value of the latest deviation vector indicating the deviation, and which adds the updated compensation vector to an offset vector to be inputted, while being superposed on said input signal, to a circuit to output said vector sum.

7. (Cancelled)

8. (Currently Amended) An offset compensating device according to claim 1,

wherein said adaptive control unit determines an inner product of two vectors which make a common angle with respect to all axes of the vector space in a quadrant in ~~the~~ a vector space where the two vectors to be determined in their inner product are individually positioned and which have a common absolute value.

9. (Currently Amended) An offset compensating device according to claim 1,

wherein said adaptive control unit sets a step size μ to be applied to said adaptive control, to the larger value as said deviation is the larger, in which said step size μ is a value indicating an updating degree of the compensation vector.

10. (Currently Amended) An offset compensating device according to claim 1,

wherein said adaptive control unit sets a step size μ to be applied to said adaptive control, to the larger value as said increment vector has the larger absolute value, in which said step size μ is a value indicating an updating degree of the compensation vector.

11. (Currently Amended) An offset compensating device according to claim 1,

wherein the larger the deviation determined in advance, said deviation monitor unit smoothes said DC component in shorter interval, ~~the more over a short section as the deviation determined in advance is the larger, thereby to obtain the deviation as the result~~ and updating the compensation vector.

12. (Currently Amended) An offset compensating device according to claim 1,

wherein the larger the absolute value of said increment vector, said deviation monitor unit smoothes said DC component in shorter interval, ~~the more over a short section as the absolute value of said increment vector is the larger, thereby to obtain the deviation as the result and~~ updating the compensation vector.

13.- 14. (Cancelled)

15. (Previously Presented) An offset compensating device according to claim 1,

wherein said adaptive control unit acts intermittently at a frequency for said compensation vector to be updated.

16. (Previously Presented) An offset compensating device according to claim 1,

wherein said adaptive control unit stops when the deviation determined in advance becomes lower than a predetermined lower limit.

17. (Previously Presented) An offset compensating device according to claim 1,

wherein said adaptive control unit stops when the absolute value of said increment vector becomes lower than a predetermined lower limit.

18. (Currently Amended) An offset compensating device according to claim 1, further comprising a dispersion monitor unit which monitors ~~the~~ a dispersion of the deviation determined in advance,

wherein said adaptive control unit stops when said dispersion becomes lower than a predetermined threshold value.

19. (Currently Amended) An offset compensating device according to claim 1, further comprising;

a dispersion monitor unit which monitors ~~the~~ a dispersion of the absolute value of said increment vector,

wherein said adaptive control unit stops when said dispersion becomes lower than a predetermined threshold value.

20. (Currently Amended) An offset compensating device according to claim 1,

wherein said deviation monitor unit monitors the deviation of said DC component with reference to ~~the~~ a DC component superposed on said input signal.

21. (Currently Amended) An offset compensating device according to claim 1, further comprising:

a quasi-offset monitor unit which detects the instant when the average of ~~the~~ a DC component superposed on said input signal becomes "0",

wherein said deviation monitor unit and said adaptive control unit start every time when said instant is detected by said quasi-offset monitor unit.

22. (Previously Presented) An offset compensating device according to claim 1,

wherein said deviation monitor unit specifies the period for which the level of the component of said vector signal in a low range for the offset caused in said circuit to distribute is lower than a predetermined lower limit, and

wherein said adaptive control unit stops for the period specified by said deviation monitor unit.

23. -28 (Cancelled)